

Figure 1

ES

sub_1	sub_2	sub_3	sub_4	sub_5	sub_6	sub_7	sub_8	sub_9	sub_10
16x 10 bits									

C_ES

e_si	e_si	e_sl	e_si	e_sl	e_sl	e_sl	e_si	e_si	e_si
_1	_2	_3	_4	_5	_6	_7	_8	_9	_10

Fig. 2A

AS

sub_1	sub_2	sub_3	sub_4	sub_5	sub_6	sub_7	sub_8	sub_9	sub_10
16x 12 bits									

C_AS

e_so	e_so	e_so	e_so	e_so	e_so	e_so	e_so	e_so	e_so
_1	_2	_3	_4	_5	_6	_7	_8	_9	_10

Fig. 2B

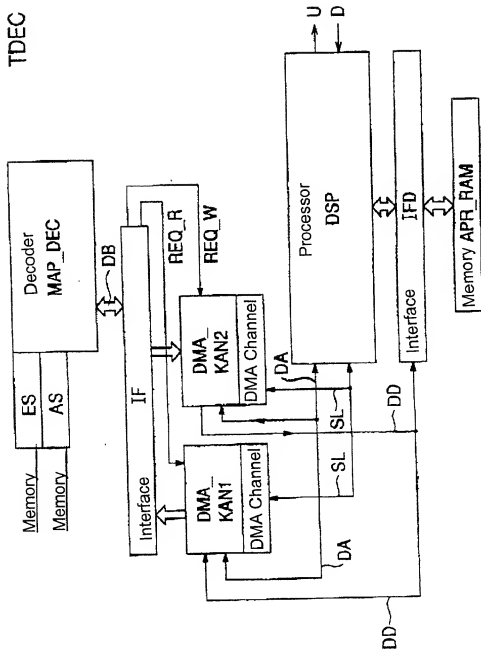


Fig. 3

4/4

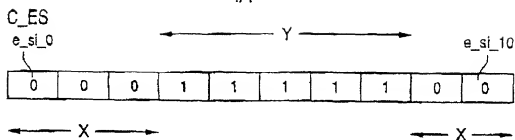


Fig. 4A

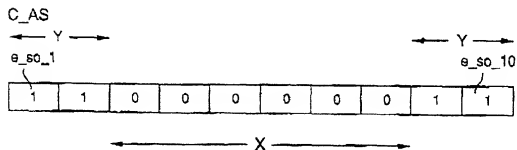


Fig. 4B

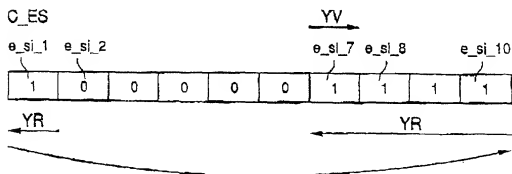


Fig. 4C

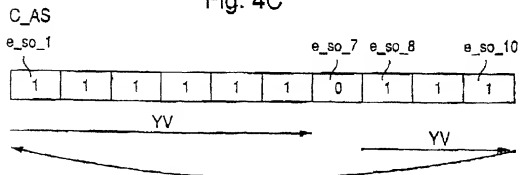


Fig. 4D